

# NGTB20N135IHRWG

## IGBT with Monolithic Free Wheeling Diode

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on-state voltage and minimal switching loss. The IGBT is well suited for resonant or soft switching applications.

### Features

- Extremely Efficient Trench with Fieldstop Technology
- 1350 V Breakdown Voltage
- Optimized for Low Losses in IH Cooker Application
- Reliable and Cost Effective Single Die Solution
- These are Pb-Free Devices

### Typical Applications

- Inductive Heating
- Consumer Appliances
- Soft Switching

### ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-emitter voltage	$V_{CES}$	1350	V
Collector current @ $T_C = 25^\circ\text{C}$ @ $T_C = 100^\circ\text{C}$	$I_C$	40 20	A
Pulsed collector current, $T_{\text{pulse}}$ limited by $T_{J\text{max}}$ , 10 $\mu\text{s}$ Pulse, $V_{GE} = 15\text{ V}$	$I_{CM}$	120	A
Diode forward current @ $T_C = 25^\circ\text{C}$ @ $T_C = 100^\circ\text{C}$	$I_F$	40 20	A
Diode pulsed current, $T_{\text{pulse}}$ limited by $T_{J\text{max}}$ , 10 $\mu\text{s}$ Pulse, $V_{GE} = 0\text{ V}$	$I_{FM}$	120	A
Gate-emitter voltage Transient Gate-emitter Voltage ( $T_{\text{pulse}} = 5\text{ }\mu\text{s}$ , $D < 0.10$ )	$V_{GE}$	$\pm 20$ $\pm 25$	V
Power Dissipation @ $T_C = 25^\circ\text{C}$ @ $T_C = 100^\circ\text{C}$	$P_D$	394 197	W
Operating junction temperature range	$T_J$	$-40$ to $+175$	$^\circ\text{C}$
Storage temperature range	$T_{\text{stg}}$	$-55$ to $+175$	$^\circ\text{C}$
Lead temperature for soldering, 1/8" from case for 5 seconds	$T_{\text{SLD}}$	260	$^\circ\text{C}$

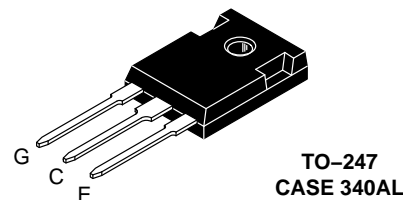
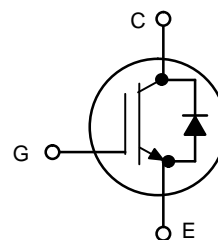
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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20 A, 1350 V  
 $V_{CE\text{sat}} = 2.20\text{ V}$   
 $E_{\text{off}} = 0.60\text{ mJ}$



### MARKING DIAGRAM



A = Assembly Location  
Y = Year  
WW = Work Week  
G = Pb-Free Package

### ORDERING INFORMATION

Device	Package	Shipping
NGTB20N135IHRWG	TO-247 (Pb-Free)	30 Units / Rail

# NGTB20N135IHRWG

## THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case	$R_{\theta JC}$	0.38	$^{\circ}\text{C/W}$
Thermal resistance junction-to-ambient	$R_{\theta JA}$	40	$^{\circ}\text{C/W}$

## ELECTRICAL CHARACTERISTICS ( $T_J = 25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
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### STATIC CHARACTERISTIC

Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE} = 0\text{ V}, I_C = 5\text{ mA}$	$V_{(BR)CES}$	1350	–	–	V
Collector-emitter saturation voltage	$V_{GE} = 15\text{ V}, I_C = 20\text{ A}$ $V_{GE} = 15\text{ V}, I_C = 20\text{ A}, T_J = 175^{\circ}\text{C}$	$V_{CEsat}$	– –	2.20 2.40	2.65 –	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_C = 250\text{ }\mu\text{A}$	$V_{GE(th)}$	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate-emitter short-circuited	$V_{GE} = 0\text{ V}, V_{CE} = 1350\text{ V}$ $V_{GE} = 0\text{ V}, V_{CE} = 1350\text{ V}, T_J = 175^{\circ}\text{C}$	$I_{CES}$	– –	– –	0.5 2.0	mA
Gate leakage current, collector-emitter short-circuited	$V_{GE} = 20\text{ V}, V_{CE} = 0\text{ V}$	$I_{GES}$	–	–	100	nA

### DYNAMIC CHARACTERISTIC

Input capacitance	$V_{CE} = 20\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	$C_{ies}$	–	5290	–	pF
Output capacitance		$C_{oes}$	–	124	–	
Reverse transfer capacitance		$C_{res}$	–	100	–	
Gate charge total	$V_{CE} = 600\text{ V}, I_C = 20\text{ A}, V_{GE} = 15\text{ V}$	$Q_g$	–	234	–	nC
Gate to emitter charge		$Q_{ge}$	–	39	–	
Gate to collector charge		$Q_{gc}$	–	105	–	

### SWITCHING CHARACTERISTIC, INDUCTIVE LOAD

Turn-off delay time	$T_J = 25^{\circ}\text{C}$ $V_{CC} = 600\text{ V}, I_C = 20\text{ A}$ $R_g = 10\text{ }\Omega$ $V_{GE} = 0\text{ V}/15\text{ V}$	$t_{d(off)}$	–	245	–	ns
Fall time		$t_f$	–	175	–	
Turn-off switching loss		$E_{off}$	–	0.60	–	mJ
Turn-off delay time	$T_J = 150^{\circ}\text{C}$ $V_{CC} = 600\text{ V}, I_C = 20\text{ A}$ $R_g = 10\text{ }\Omega$ $V_{GE} = 0\text{ V}/15\text{ V}$	$t_{d(off)}$	–	270	–	ns
Fall time		$t_f$	–	290	–	
Turn-off switching loss		$E_{off}$	–	1.40	–	mJ

### DIODE CHARACTERISTIC

Forward voltage	$V_{GE} = 0\text{ V}, I_F = 20\text{ A}$ $V_{GE} = 0\text{ V}, I_F = 20\text{ A}, T_J = 175^{\circ}\text{C}$	$V_F$	– –	1.80 2.70	2.10 –	V
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Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## TYPICAL CHARACTERISTICS

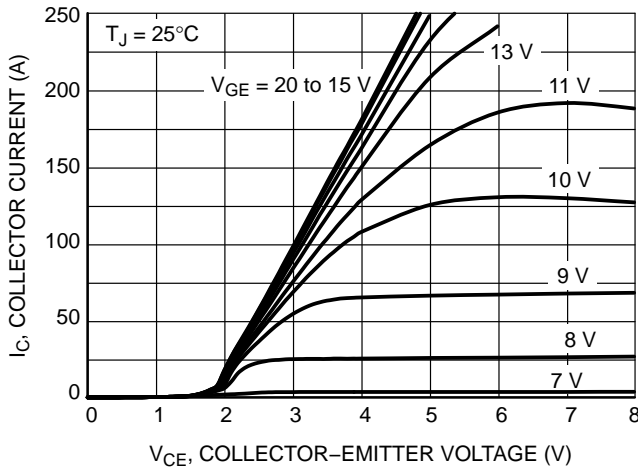


Figure 1. Output Characteristics

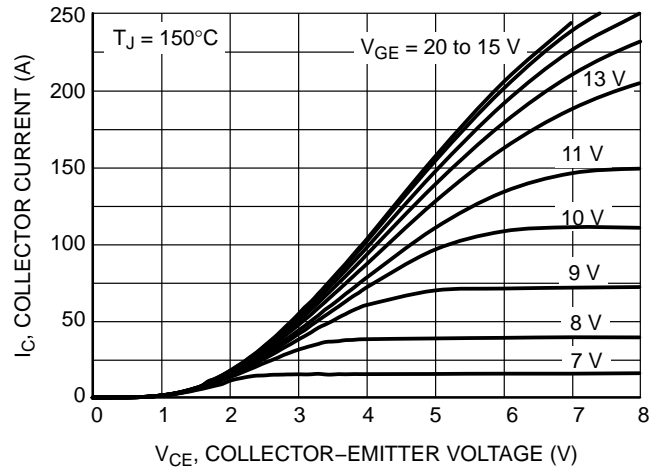


Figure 2. Output Characteristics

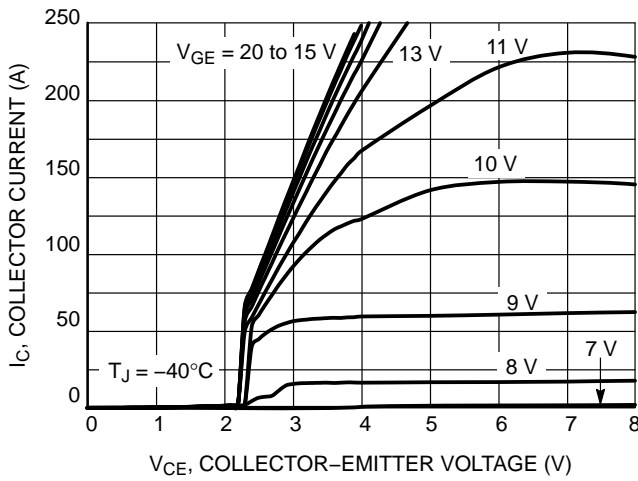


Figure 3. Output Characteristics

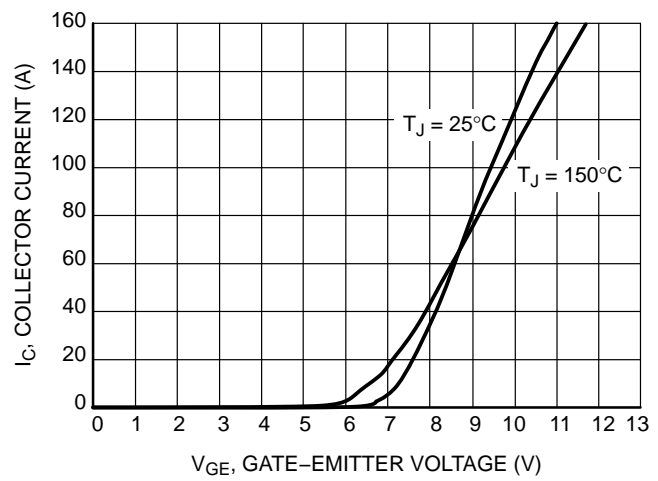


Figure 4. Typical Transfer Characteristics

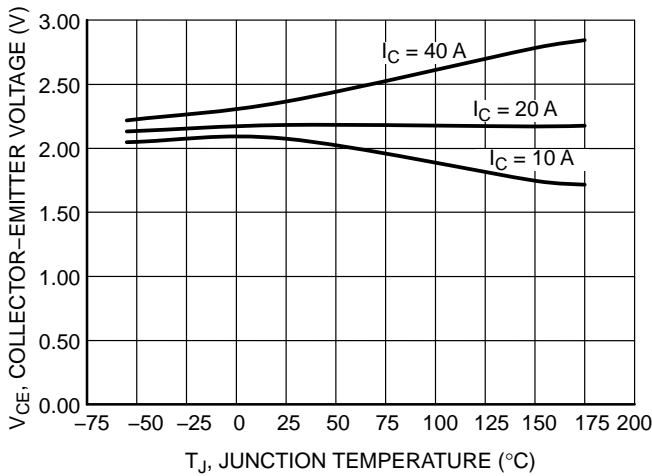


Figure 5.  $V_{CE(sat)}$  vs.  $T_J$

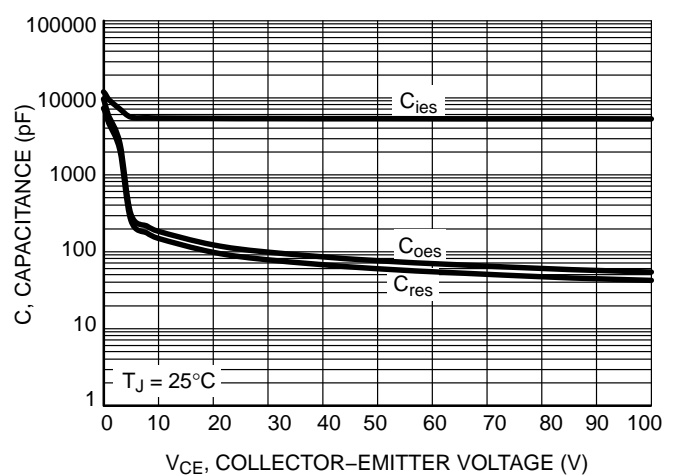


Figure 6. Typical Capacitance

TYPICAL CHARACTERISTICS

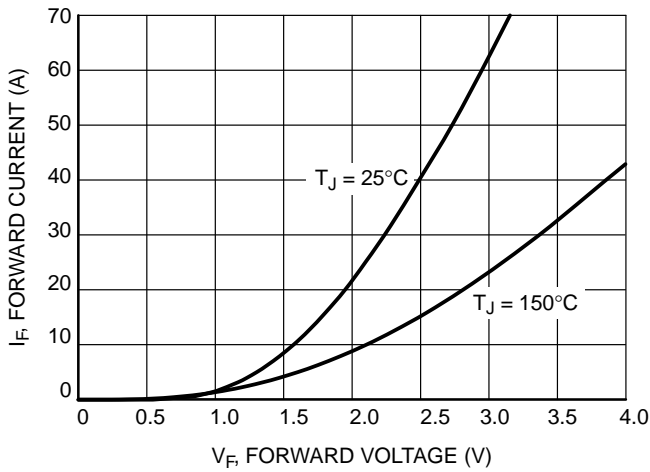


Figure 7. Diode Forward Characteristics

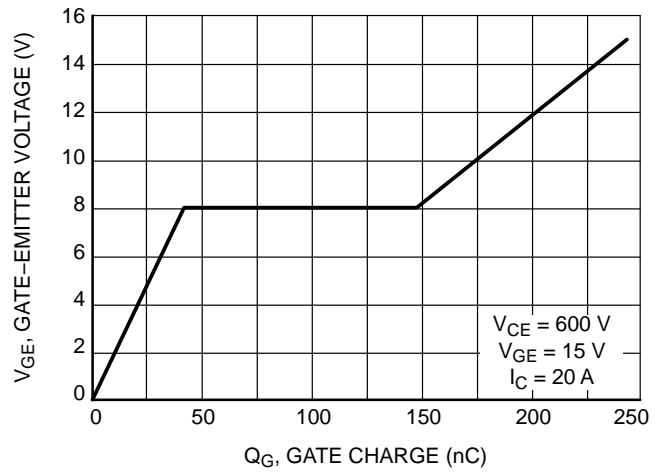


Figure 8. Typical Gate Charge

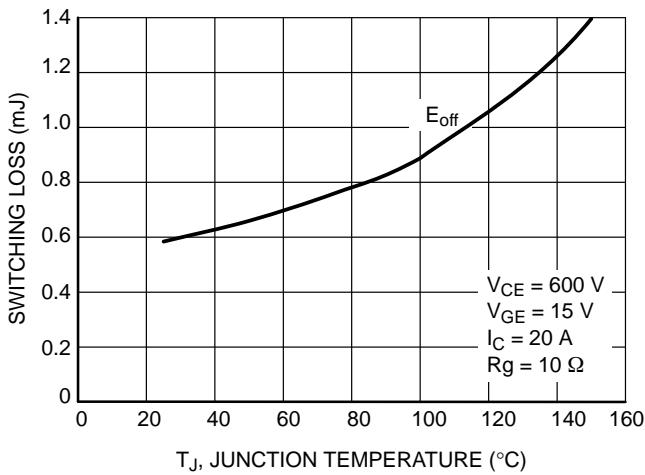


Figure 9. Switching Loss vs. Temperature

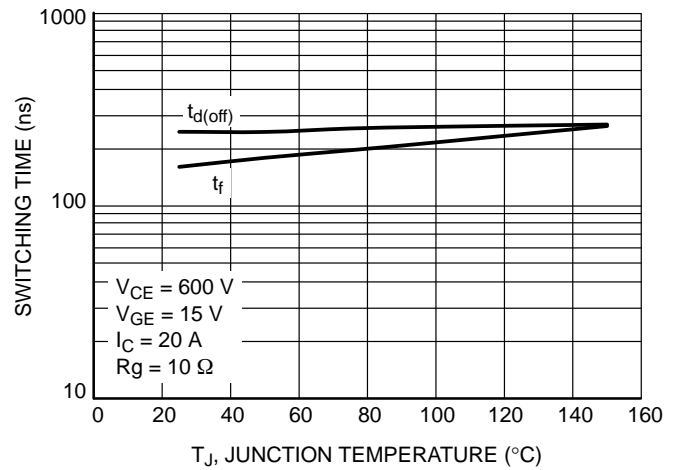


Figure 10. Switching Time vs. Temperature

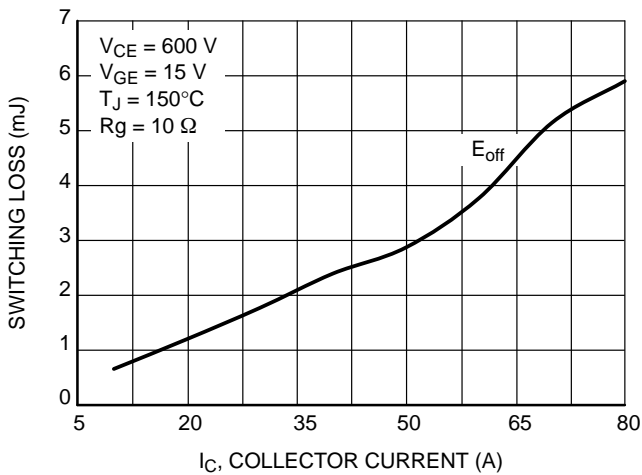


Figure 11. Switching Loss vs.  $I_C$

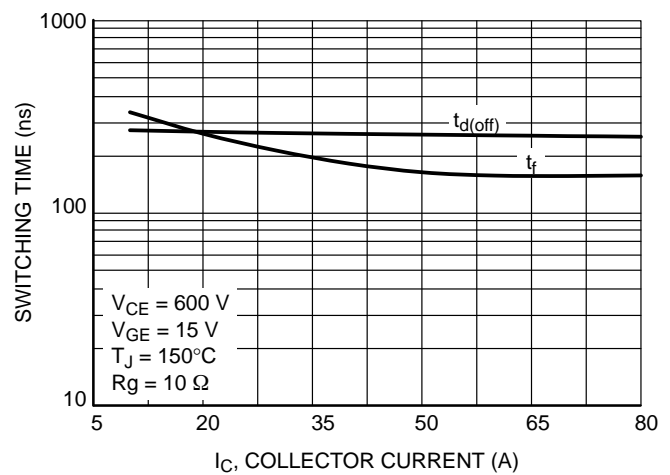


Figure 12. Switching Time vs.  $I_C$

TYPICAL CHARACTERISTICS

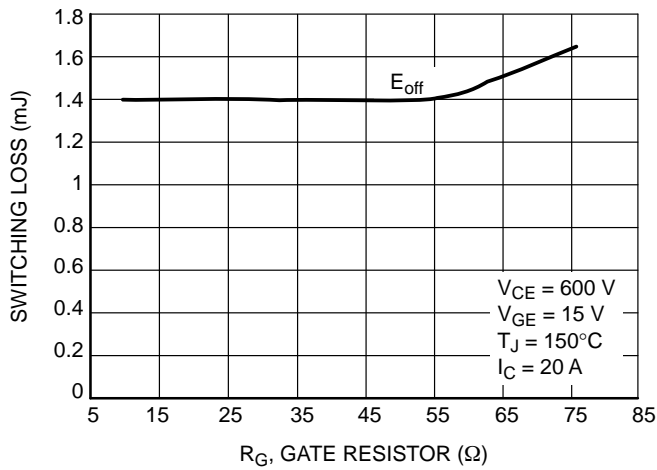


Figure 13. Switching Loss vs.  $R_g$

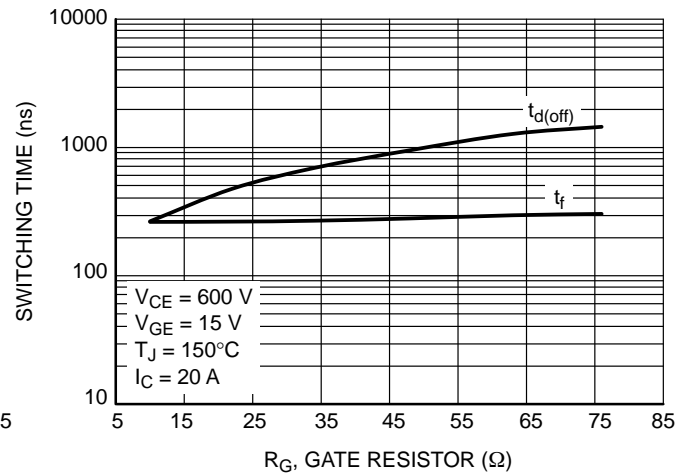


Figure 14. Switching Time vs.  $R_g$

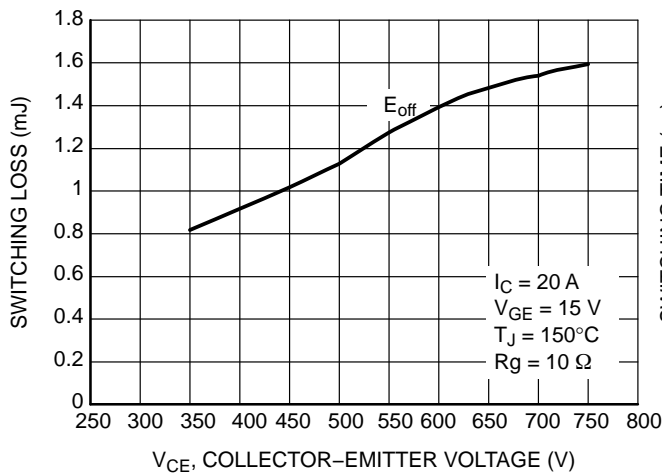


Figure 15. Switching Loss vs.  $V_{CE}$

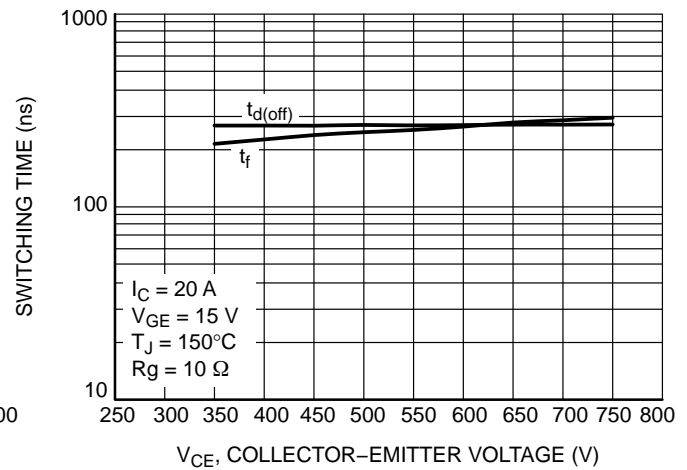


Figure 16. Switching Time vs.  $V_{CE}$

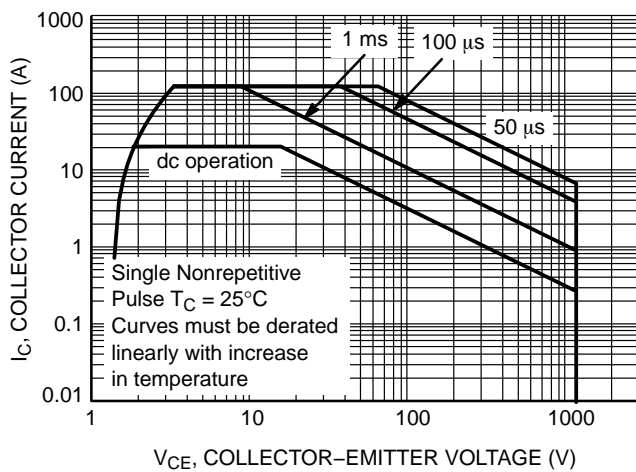


Figure 17. Safe Operating Area

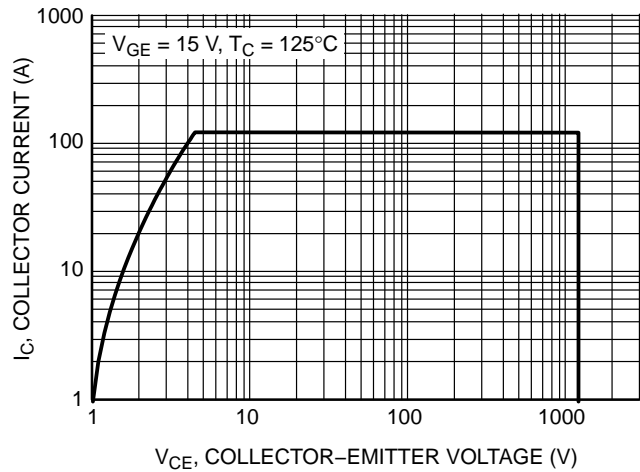


Figure 18. Reverse Bias Safe Operating Area

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## TYPICAL CHARACTERISTICS

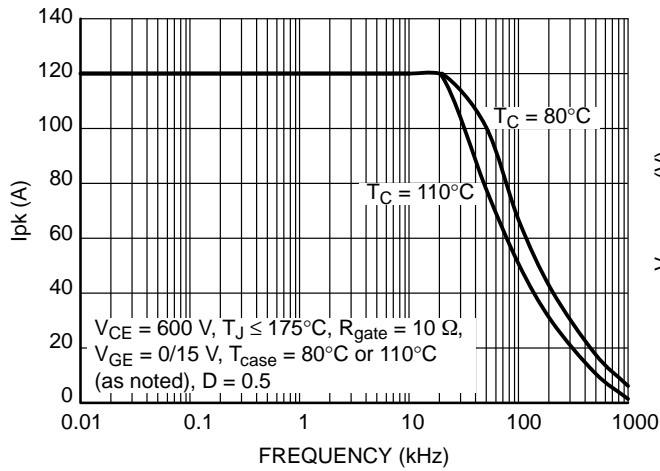


Figure 19. Collector Current vs. Switching Frequency

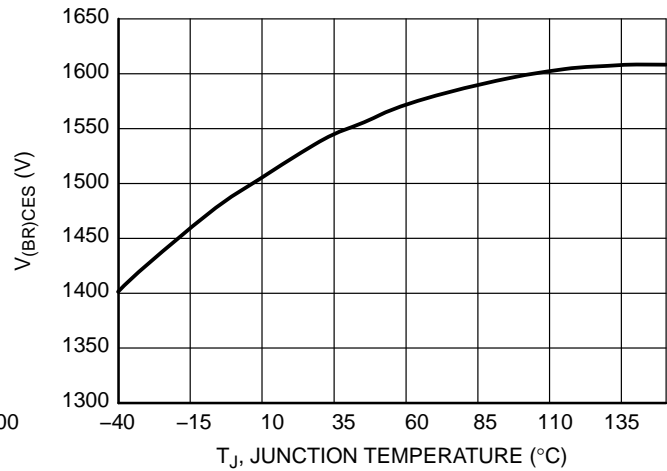


Figure 20. Typical  $V_{(BR)CES}$  vs. Temperature

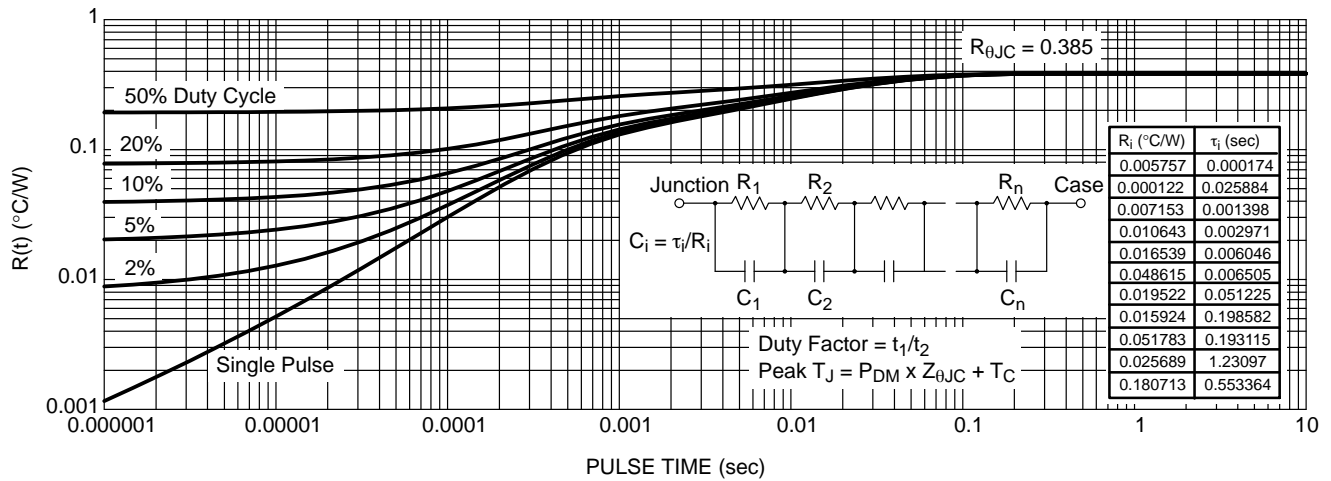


Figure 21. IGBT Transient Thermal Impedance

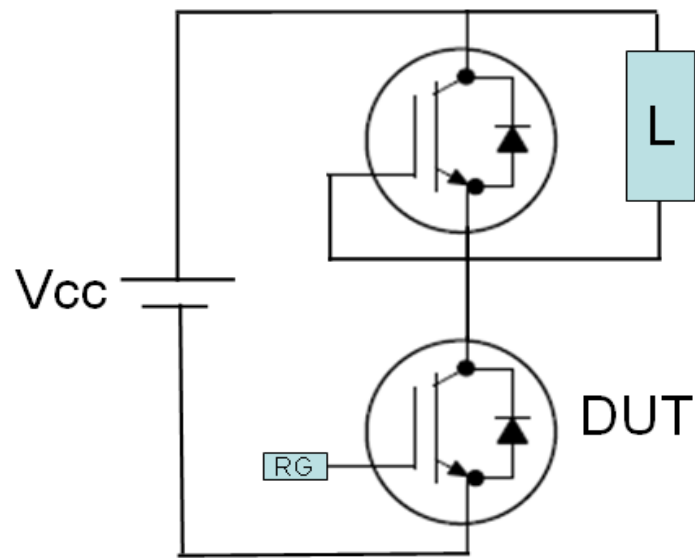


Figure 22. Test Circuit for Switching Characteristics



Figure 23. Definition of Turn On Waveform



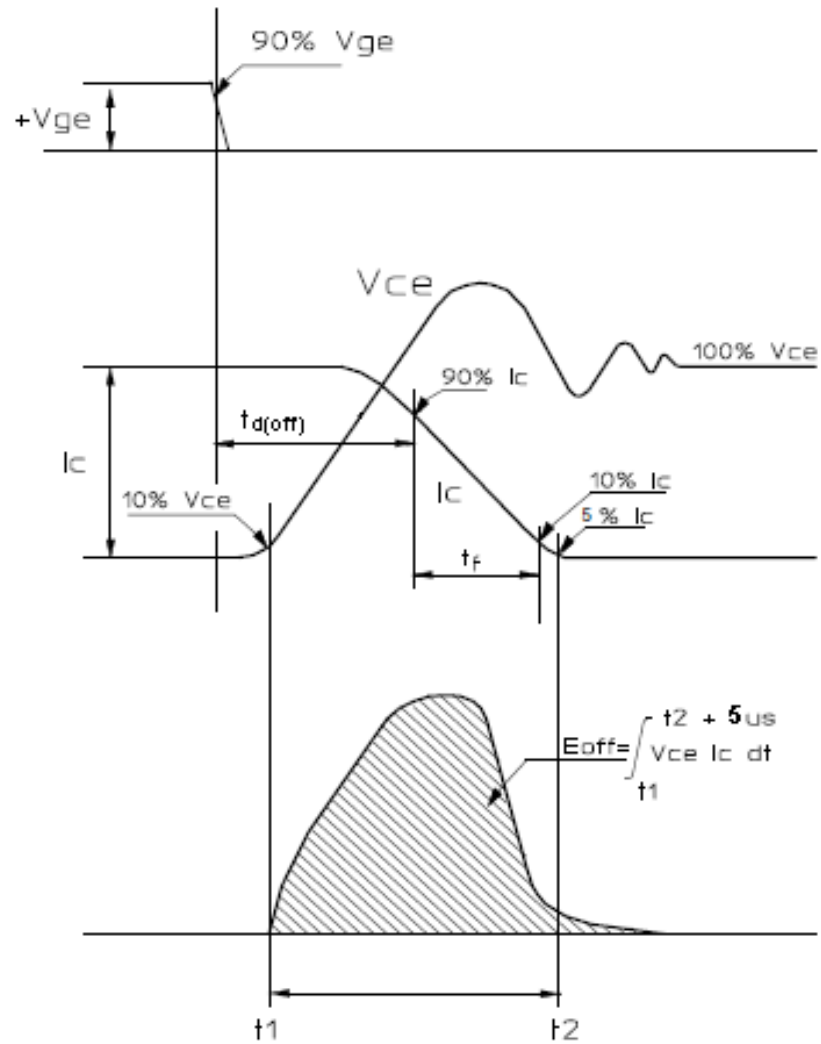
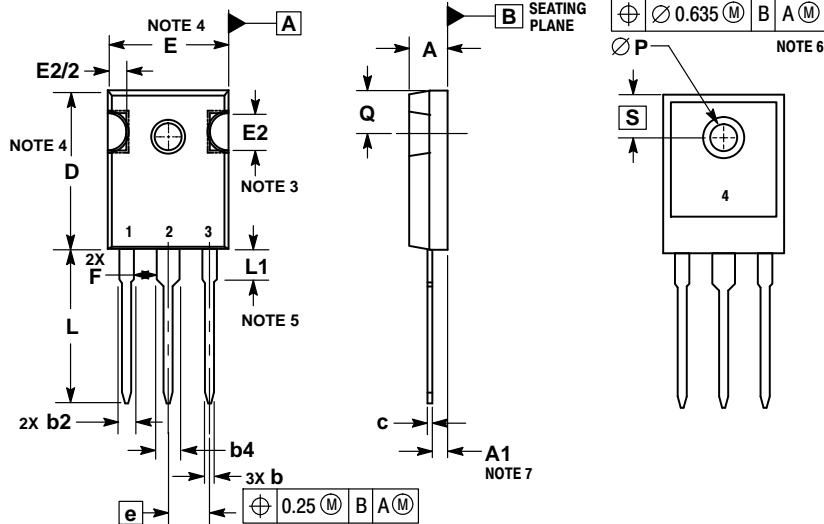


Figure 24. Definition of Turn Off Waveform

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## PACKAGE DIMENSIONS

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